

QSFP-DD EDFA

Features

- QSFP-DD form factor
- Support dual OA
- Fully-controlled module with communication interface
- Support AGC/APC mode
- Low power consumption



Applications

- DCI

Main Specifications

Parameter	Max	Min	Specification	Unit
Operating Wavelength			1539.1~1558.4	nm
Input Power			-24 ~ 10	dBm
Output Power	√		17	dBm
Gain			7 ~ 25	dB
Noise Figure at Flat Gain (G=19dB)	√		6	dB
Gain Ripple @ Flat Gain	√		0.8	dB
PDG	√		0.5	dB
PMD	√		0.5	ps
Input/ Output Return Loss		√	40	dB
Communication Interface			I2C/UART	
Power Supply Voltage			3.3	V
Operating Case Temperature			-5~70	°C
Storage Temperature			-40~80	°C
Operating Humidity			5~95	%
Dimensions			93*18.35*12	mm

Electronic Connector Pin Assignment

BOTTOM SIDE		TOP SIDE	
Pin#	Description	Pin#	Description
1	Ground	20	Ground
2	NC	21	NC
3	NC	22	NC

4	Ground	23	Ground
5	NC	24	NC
6	NC	25	NC
7	Ground	26	Ground
8	Module select input	27	Module OA2sent
9	Module reset input	28	Module TMP alarm
10	3.3V power supply	29	3.3V power supply
11	2-wire serial interface clock	30	3.3V power supply
12	2-wire serial interface data	31	OA2 disable
13	Ground	32	Ground
14	NC	33	NC
15	NC	34	NC
16	Ground	35	Ground
17	NC	36	NC
18	NC	37	NC
19	Ground	38	Ground
39	Ground	58	Ground
40	NC	59	NC
41	NC	60	NC
42	Ground	61	Ground
43	NC	62	NC
44	NC	63	NC
45	Ground	64	Ground
46	Connect to MCU GPIO	65	NC
47	Connect to MCU GPIO	66	Module TMP alarm
48	3.3V power supply input	67	3.3V power supply
49	Connect to MCU GPIO	68	3.3V power supply
50	Connect to MCU GPIO	69	OA2 disable
51	Ground	70	Ground
52	NC	71	NC
53	NC	72	NC
54	Ground	73	Ground
55	NC	74	NC
56	NC	75	NC
57	Ground	76	Ground

Order Information

The above specifications represent the typical performance of O-Net Amplifier of this category. Please contact our Sales to discuss your specific requirements.